

Allegro Power Integrated Circuit Package Portfolio

Motor Driver and Interface ICs | Regulators and Lighting

Allegro MicroSystems, LLC is a leader in developing, manufacturing and marketing high-performance semiconductors. Allegro's innovative solutions serve high-growth applications within the automotive market, with additional focus on office automation, industrial, and consumer/communications solutions.

Allegro's power IC packages offer industry-leading thermal performance with limited board space.

TSSOP – Industry-standard TSSOP with optional exposed pad for enhanced thermal performance

QFP – Universal quad flat pack with exposed pad for enhanced thermal performance

QFN/TDFN – Quad and dual, low-profile, surface-mount packages with exposed pad for enhanced thermal performance

MSOP – Industry-standard miniature small outline package with optional exposed pad for enhanced thermal performance

SOIC – Small outline integrated circuit with optional exposed pad for enhanced thermal performance

CSP – Wafer level chip scale

Additional industry-standard packaging options are available to meet individual design requirements.

The package options shown below are a sampling of Allegro's most recent, high technology packages. For specific package information, please visit Allegro's website at:

www.allegromicro.com/packaging.

With Leads

JP (QFP with exposed pad)

Terminals: 32, 48

Size: 7 x 7 mm body width



LP (TSSOP with exposed pad)

Terminals: 16-38

Size: 4.4 mm body width



LB (SOIC)

Terminals: 16-28

Size: 7.5 mm body width



LJ (SOIC with exposed pad)

Terminals: 8-10

Size: 3.9 mm body width



LZ (MSOP with exposed pad)

Terminals: 10

Size: 3 mm body width



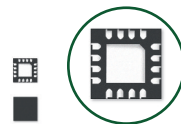
Without Leads

ES, EC, ET, EU, EV

(QFN with exposed pad)

Terminals: 16-48

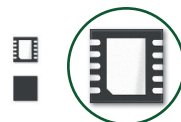
Size: 3 x 3 mm body width to
7 x 7 mm body width



EJ (TDFN with exposed pad)

Terminals: 3-16

Size: 2 x 2 mm body to
3 x 3 mm body width



Chip Scale

CG (Chip Scale)

Terminals: 4-12



Please Note: Package sizes are photographed to show relative scale.



www.allegromicro.com/camp661

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